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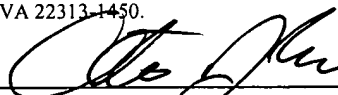
Applicant(s): Byung-jae Lee, *et al.*
Filing Date: Herewith
Title: MEMORY DEVICE, AND METHOD OF AMPLIFYING VOLTAGE
LEVELS OF BIT LINE AND COMPLEMENTARY BIT LINE

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4-21-04

Date



Christopher Hren

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to prosecution, please amend the application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 9 of this paper.